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(72) Inventors:  
• **Subhash, Gupta**  
**Singapore 259805 (SG)**  
• **Mei-Sheng, Shou**  
**129791 Singapore (SG)**

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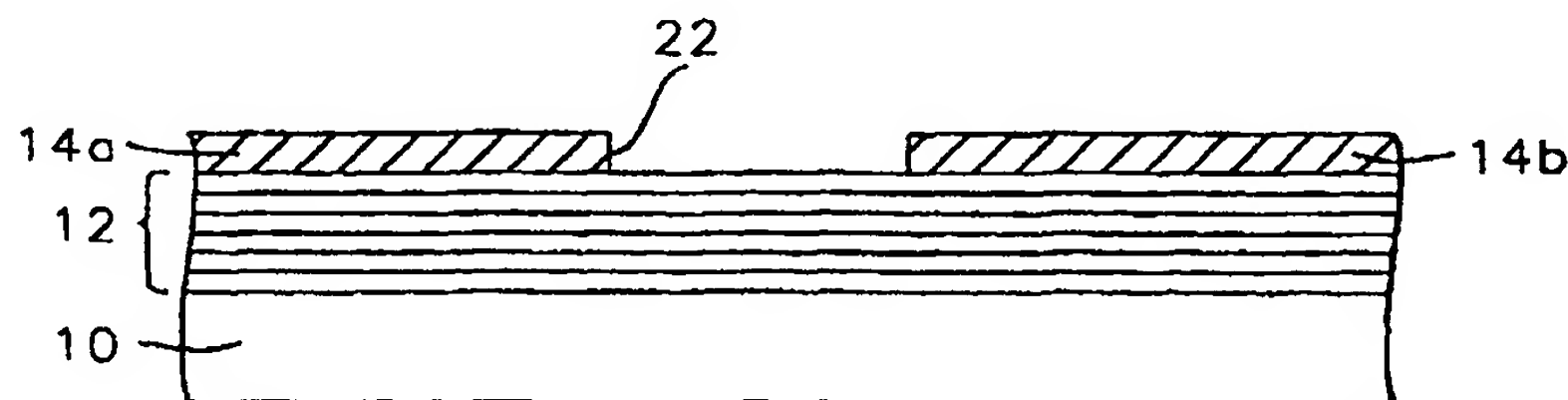
(74) Representative: **Schuffenecker, Thierry**  
**97, chemin de Cassiopée,**  
**Domaine de l'étoile**  
**06610 La Gaude (FR)**

(71) Applicant: **CHARTERED SEMICONDUCTOR**  
**MANUFACTURING, INC.**  
**Milpitas, CA 95035 (US)**

(54) **Method of fabricating mask for extreme ultraviolet radiation**

(57) An EUV photolithographic mask device and a method of fabricating the same. The EUV photolithographic mask comprises a multi-layer over an EUV masking substrate and a patterned light absorbing layer formed on the multi-layer. The method comprises the steps of forming a multi-layer on an EUV mask sub-

strate, forming a light absorbing layer on the multi-layer, and etching an opening through the light absorbing layer to the multi-layer. The light absorbing layer includes a metal selected from the group comprising nickel, chromium, cobalt, and alloys thereof, and is preferably nickel.



**FIG. 8**

## Description

### Technical field

5 [0001] The present invention relates generally to photolithographic mask fabrication, and specifically to a method of extreme ultraviolet mask engineering.

### Background art

10 [0002] Extreme ultraviolet is the most commonly accepted solution for next generation lithography (NGL). The mask is a reflective system having up to 50 pairs of multi-layer materials and the absorber layer patterned with low temperature processes. The current two basic EUV mask patterning approaches (Direct Metal Patterning and Damascene Process) involves multi-steps of etching/film deposition/ chemical mechanical polishing (CMP) process.

15 [0003] U.S. Patent No. 5,958,629 to Yan et al. describes a method of fabricating EUV masks by forming an etch stop layer over the surface of a mask to create a more controllable etch profile for etching patterns into the material above the etch stop layer.

[0004] U.S. Patent No. 5,935,737 to Yan describes a method of fabricating EUV masks using dual defect-absorbing layers to ensure that through two steps of repair, the repair stains are eliminated on that section of the mask which must cleanly reflect light in the case of a reflective mask, or transmit light in the case of a transmissive mask.

20 [0005] U.S. Patent No. 5,521,031 to Tenant et al. describes a method of fabricating EUV masks by incorporating the operating principle of the attenuated phase mask in a reflecting structure. The apparatus serves as an alternative, or supplement, to a surface-activated resist to permit projection-reduction lithography with improved image edge definition.

### 25 Summary of the invention

[0006] Accordingly, it is an object of the present invention to provide EUV masks and a method of fabricating the EUV masks using less processing steps.

30 [0007] Another object of the present invention is to provide EUV masks and a method of fabricating the EUV masks using low processing temperatures.

[0008] Yet another object of the present invention is to provide EUV masks and a method of fabricating the EUV masks with minimum sub-layer damage.

[0009] A further object of the present invention is to provide an easier and more economic method of fabricating EUV masks.

35 [0010] Another object of the present invention is to use nickel (Ni) as an absorber layer instead of chromium (Cr) allowing for a relatively low temperature etch.

[0011] Other objects will appear hereinafter.

40 [0012] It has now been discovered that the above and other objects of the present invention may be accomplished in the following manner. The EUV photolithographic mask comprises a multi-layer over an EUV masking substrate and a patterned light absorbing layer formed on the multi-layer. The method comprises the steps of forming a multi-layer on an EUV mask substrate, forming a light absorbing layer on the multi-layer, and etching an opening through the light absorbing layer to the multi-layer. The light absorbing layer includes a metal selected from the group comprising nickel, chromium and cobalt and is preferably nickel.

### 45 Description of the drawings

[0013] The features and advantages of the method of fabricating EUV masks according to the present invention will be more clearly understood from the following description taken in conjunction with the accompanying drawings in which like reference numerals designate similar or corresponding elements, regions and portions and in which:

50 Figs. 1 and 2 schematically illustrate in cross-sectional representation a Direct Metal Patterning Process of forming EUV masks known to the inventors.

55 Figs. 3 and 4 schematically illustrate in cross-sectional representation a Damascene Process of forming EUV masks known to the inventors.

Figs. 5 through 8 schematically illustrate in cross-sectional representation a preferred embodiment of the present invention.

**Description of the preferred embodiments of the invention**

[0014] Unless otherwise specified, all structures, layers, etc. may be formed or accomplished by conventional methods known in the prior art. While not limited to a particular application, the invention is particularly useful for extreme ultraviolet lithography (EUVL) that is preferably conducted at wavelength of between 3 and 50 nanometers (nm) and is intended for use in chip fabrication where minimum line widths of  $\leq 0.07$  micron are required.

Process One

[0015] Figs 1 and 2 illustrate a Direct Metal Patterning Process of forming EUV masks known to the inventors.

[0016] As shown in Fig. 1, reflector layer 202 is formed over mask blank 200. Buffer layer 204, that may be comprised of  $\text{SiO}_2$ , is then deposited over reflector layer 202. Metal layer 206, that may be comprised of Al, TaSi, Cr, SiON, Ti, of TiN, is deposited over buffer layer 204.

[0017] As shown in Fig. 2, metal layer 206 is then patterned by, for example, depositing and patterning a photoresist layer (not shown) over metal layer 206, then etching metal layer 206 to form metal layer trench 208 exposing a portion of underlying buffer layer 204. The exposed portion of buffer layer 204 is then etched to form buffer layer trench 210 beneath metal layer trench 208.

[0018] Optionally (not shown) a layer of silicon (Si) may be deposited and chemically mechanically polished (CMP) to complete formation of the Direct Metal Patterning photolithographic mask.

Process Two

[0019] Figs. 3 and 4 illustrate a Damascene Process of forming EUV masks known to the inventors.

[0020] As shown in Fig. 3, a multi-layer reflector layer 302 is formed over mask blank 300. Silicon cap layer 304 is then formed over reflector layer 302. Silicon cap layer 304 is then etched to form trench 306 leaving bottom portion 308 of silicon cap layer beneath trench 306.

[0021] The structure is then inspected for defects and any necessary repairs are effected.

[0022] A metal layer (not shown) is deposited over the structure, filling trench 306, and planarized by chemical mechanical polishing (CMP) to form planarized metal portion 310 over silicon cap layer portion 308.

[0023] Optionally (not shown) a layer of silicon (Si) may be deposited and chemically mechanically polished (CMP) to complete formation of the Damascene photolithographic mask.

[0024] However, both the Direct Metal Patterning Process of Figs. 1 and 2 and the Damascene Process of Figs. 3 and 4 in forming photolithographic masks involve multiple steps of (at least) etching, stripping of a patterned photoresist layer (not shown), deposition of a film, and planarization of the film by chemical mechanical polishing.

[0025] Further, it is a great challenge to make EUV masks with low temperature, low defects, and no multi-layer reflector layer damage.

**PREFERRED EMBODIMENT OF THE PRESENT INVENTION**

[0026] Accordingly as shown in Figs. 5 - 8, multi-reflector layer 12 is formed over mask blank 10 to form a mask substrate. Mask blank 10 is any material suitable for the type of photolithography wavelength regime to be used. For extreme ultraviolet lithography (EUVL), mask blank 10 is preferably either silicon or fused silica with a highly polished surface..

[0027] Reflector layer 12 preferably consists of multiple layers of alternating reflecting material and transmissive material. Various combinations of reflective and transmissive materials may be used such as Mo/Si, Ru/C, Ru/B<sub>4</sub>C, Mo/Be, etc. The reflector layer is preferably comprised of molybdenum (Mo) and silicon (Si) layers (Mo/Si), each layer with a thickness of about  $(\lambda/2)/(\theta)$ . Where  $\lambda$  is the wavelength of the light, and  $\theta$  is the incident angle. The reflector layer 12 has preferably from about 40 to 50 paired layers.

[0028] Light absorbing layer 14 is deposited over reflector layer 12 preferably to a thickness of from about 200 to 500Å, and more preferably from about 250 to 350Å. Light absorbing layer 14 is comprised of a material selected for its characteristics of being absorptive to the wavelength of light to be used and for its ease in mask fabrication.

[0029] Specifically, light absorbing layer 14 may be comprised of a metal selected from the group nickel (Ni), chromium (Cr), and cobalt (Co) and alloys thereof.

[0030] In a key step of the invention, light absorbing layer 14 is most preferably comprised of nickel (Ni) as will be used hereafter for the purposes of example. The use of a nickel light absorbing layer 14 permits a low temperature etch.

[0031] Light absorbing layer 14 is then patterned, for example, as shown in Fig. 5, by deposited a suitable photoresist (PR) layer 16 over Ni layer 14 to an appropriate thickness.

[0032] As shown in Fig. 6, PR layer 16 is patterned forming PR trench 18, and exposing portion 20 of Ni layer 14.

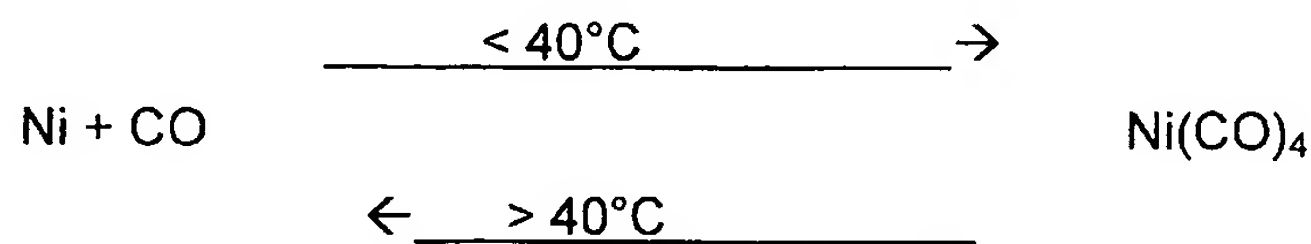
PR layer 16 is split into portions 16a, 16b. The photoresist is exposed and developed using conventional processes.

[0033] As shown in Fig. 7, in an important step in the present invention, light absorbing layer 14 is etched by a highly selective, low temperature, low power and with no bias power to minimize to the bombardment. The etch splits layer 14 into portions 14a, 14b and forming light absorbing layer trench 22. That is, the temperature is lower than about 40°C and preferably between about 0 to 39°C; the inductive coil power is less than about 350 watts (W) and preferably between about 100 to 200 watts; preferably at a CO gas flow between about 50 and 400 sccm, more preferably between about 200 and 350 sccm, and most preferably about 300 sccm; preferably from about 8 to 12 milli torr and more preferably about 10 milli torr, and with no bias power being applied to minimize the bombardment..

[0034] This etch is conducted by a reactive ion etching (RIE) plasma etcher, such as the IPS, IEM, DRM, or TCP etcher and preferably the Lam transformer-coupled plasma source TCP9100 PTX. The etching conditions depend upon the specific plasma etcher selected.

[0035] This etch does not create a side-wall polymer because the etch products are very volatile at the low temperature at which the etch is possible because nickel is selected as comprising light absorbing layer 14.

[0036] The reaction during the Ni layer etch is:



[0037] As shown in Fig. 8, remaining PR layer portions 16a, 16b are stripped and removed. The PR strip may be accomplished by ozone (O<sub>3</sub>) injected into hot water, that is by the Hydrozone Process by Fluorowave System Inc. (FSI) with the water at 85°C. Alternatively, the PR strip can be accomplished by a SO<sub>3</sub> strip process developed by Anon Inc. at ≤ 50°C.

[0038] The structure is then subject to defect inspection and repair by any standard technique using any standard tool.. Any defects are identified visually by type, clear or opaque, and by location. An ion beam is used, usually in a focused gallium ion beam system, to either create patches in the structure or remove unwanted material arising from any such defects.

[0039] The ability to use the low temperature, low power and minimum bombardment etch of nickel light absorbing layer 14 allows one to retain the reflecting properties of the mirror (mask) due to the absence of thermal interdiffusion between the layers, thus permitting fabrication of quality EUV photolithographic masks.

## Claims

1. A method of fabricating a photolithographic EUV mask device, comprising the steps of:

forming a multi-layer on an EUV mask substrate;  
forming a light absorbing layer on said multi-layer; said light absorbing layer including a metal selected from the group comprising nickel, chromium, cobalt, and alloys thereof; and  
etching an opening through said light absorbing layer to said multi-layer.

2. The method of claim 1, wherein said light absorbing layer is comprised of nickel.

3. The method of claim 1, wherein said light absorbing layer is from about 200 to 500Å thick.

4. The method of claim 1, wherein said light absorbing layer is from about 250 to 350Å thick.

5. The method of claim 1, wherein said opening etching is conducted below about 40° C in an conductivity coupled RF plasma etcher comprising CO gas

6. A method of fabricating a photolithographic EUV mask device, comprising the steps of:

forming a multi-layer on an EUV mask substrate;  
forming a metal layer on said multi-layer; said metal layer being comprised of a metal selected from the group

comprising nickel, chromium, cobalt, and alloys thereof;  
forming a photoresist layer over said metal layer;  
patterning said photoresist layer exposing a portion of said metal layer;  
etching said exposed portion of said metal layer forming an opening through said metal layer to said EUV  
mask substrate.

7. The method of claim 6, wherein said metal layer is comprised of nickel.

8. The method of claim 6, wherein said metal layer is from about 200 to 500Å thick.

9. The method of claim 6, wherein said metal layer is from about 250 to 350Å thick..

10. The method of claim 1 or 6, wherein said mask substrate includes a reflective surface.

11. A photolithographic mask structure, comprising:

a masking substrate;  
a multi-layer over said masking substrate; and  
a patterned light absorbing layer on said mask substrate; said light absorbing layer being comprised of a metal  
selected from the group comprising nickel, chromium, cobalt, and alloys thereof.

12. The mask structure of claim 11, wherein said light absorbing layer is comprised of nickel.

13. The mask structure of claim 11, wherein said light absorbing layer is from about 200 to 500Å thick.

14. The mask structure of claim 11, wherein said light absorbing layer is from about 250 to 350Å thick..

15. The mask structure of claim 11, wherein said mask substrate includes a reflective surface.

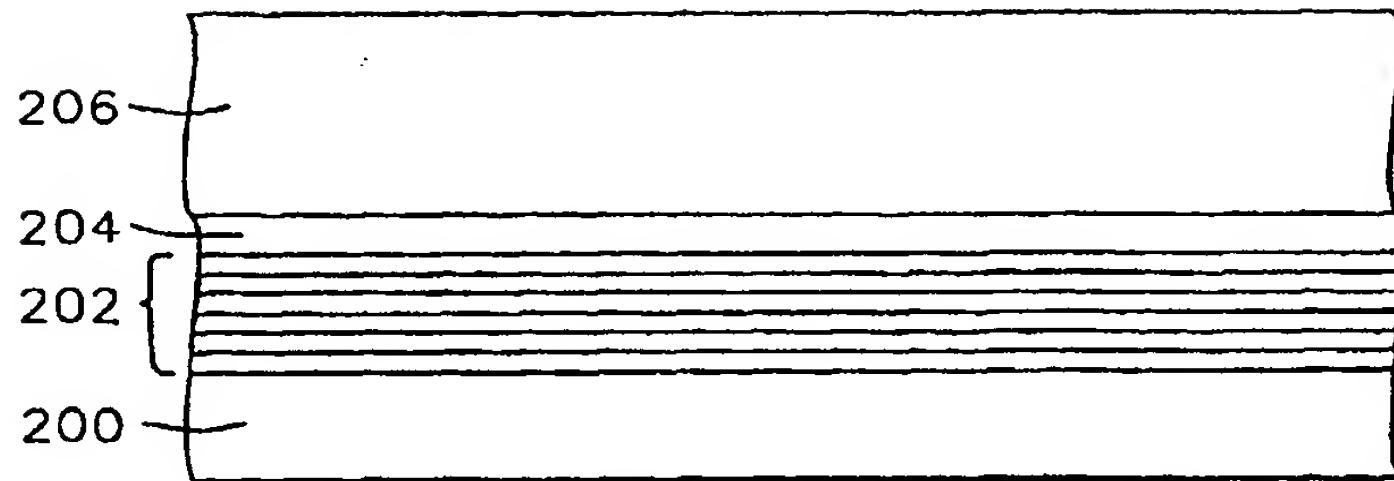


FIG. 1

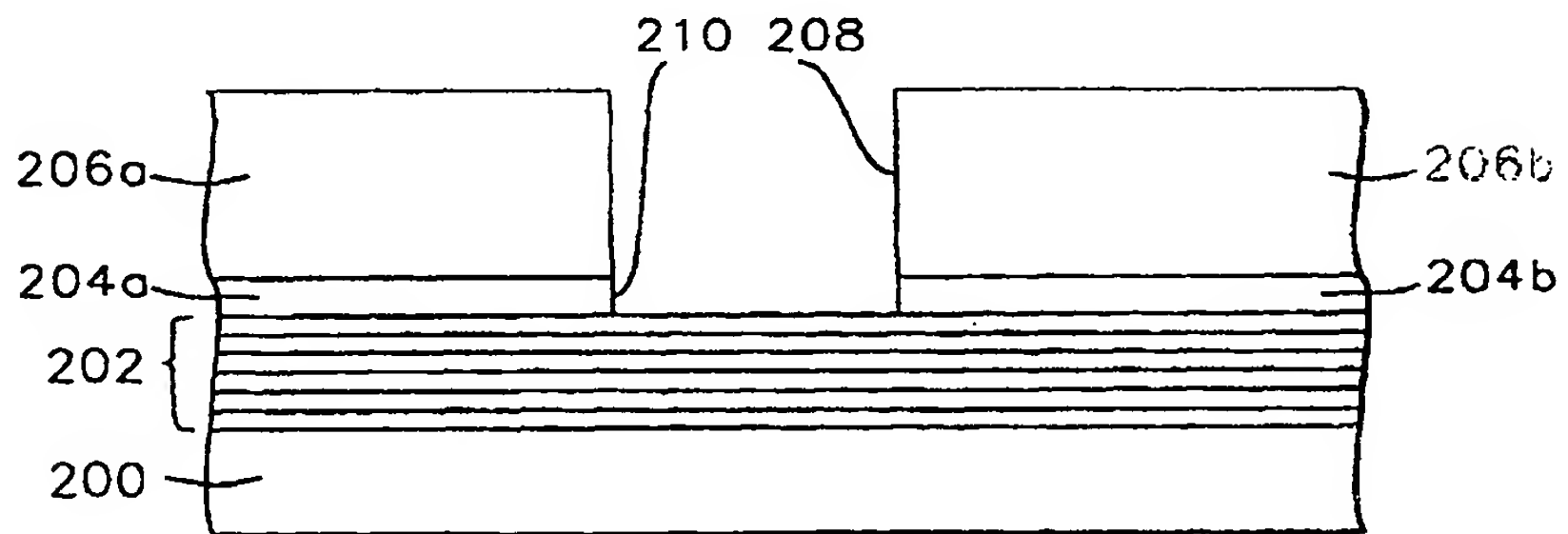


FIG. 2

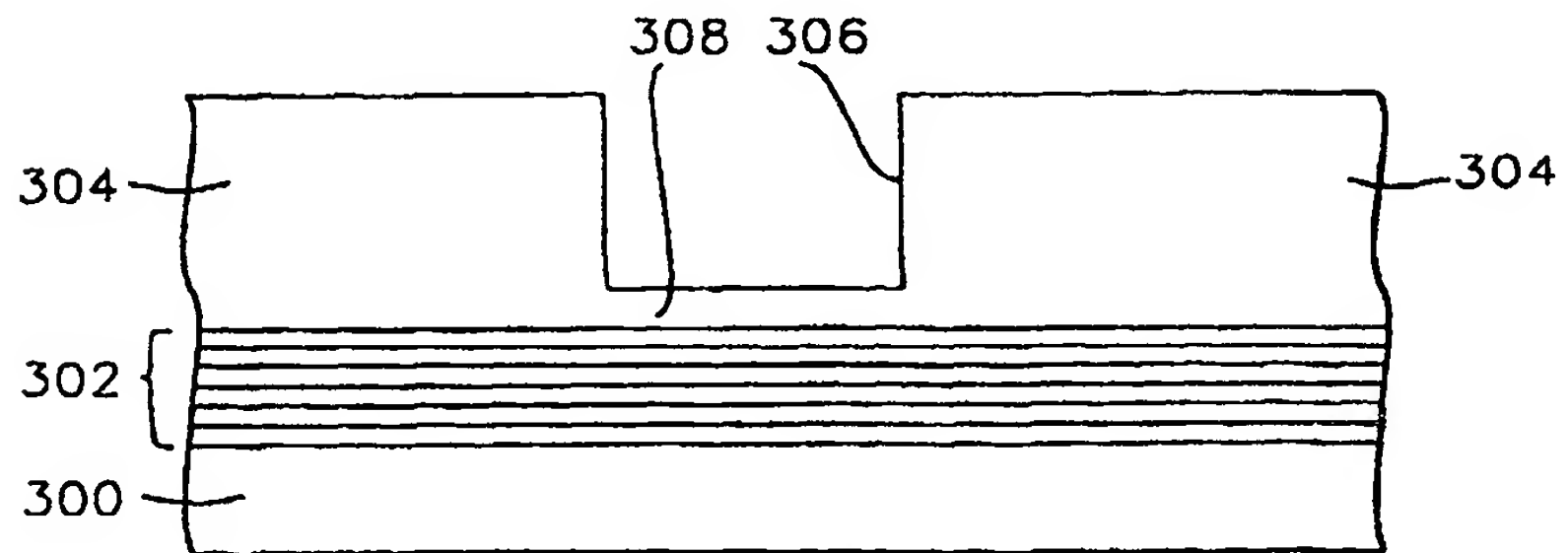


FIG. 3

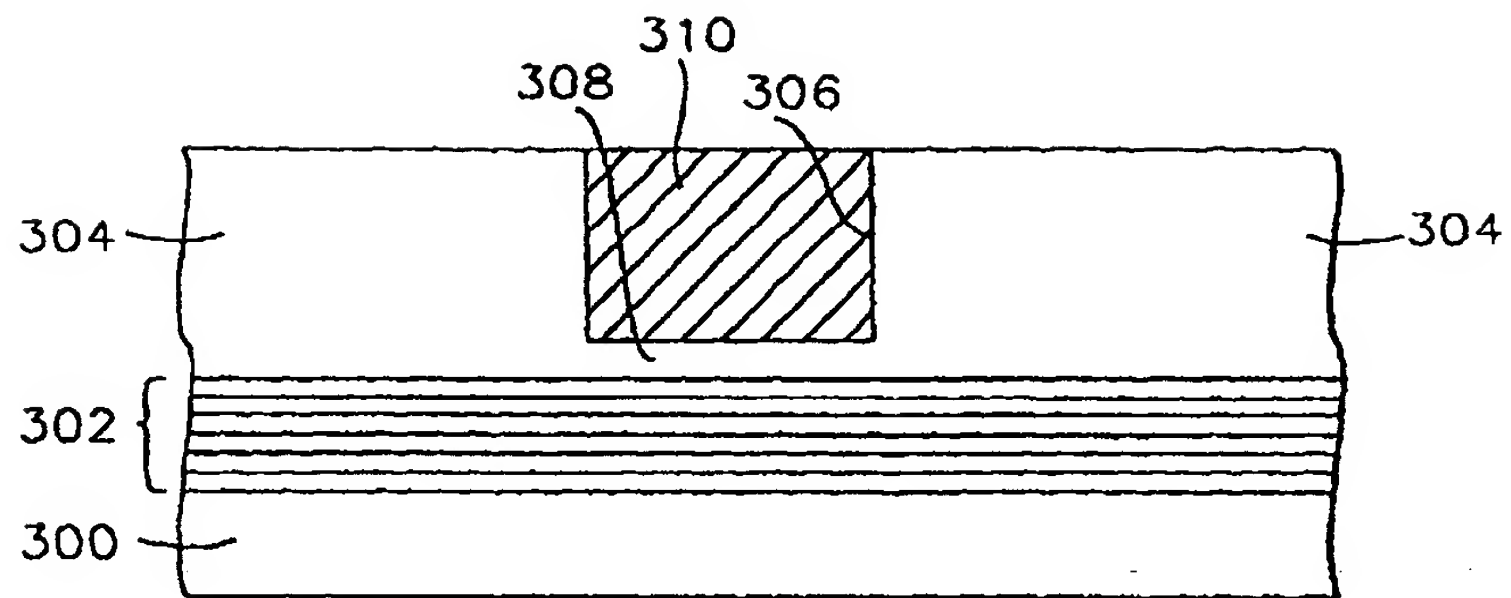


FIG. 4

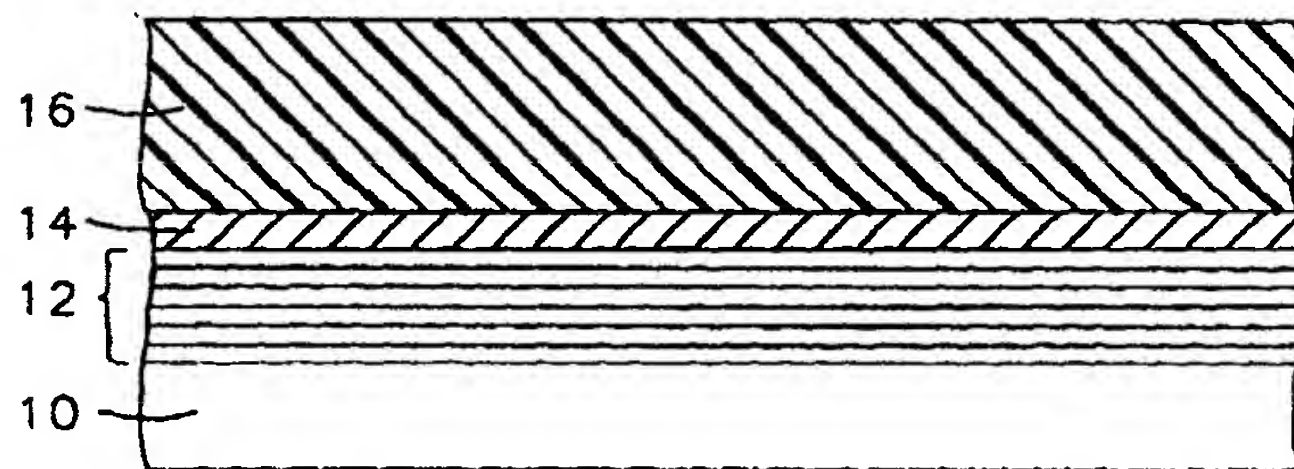


FIG. 5

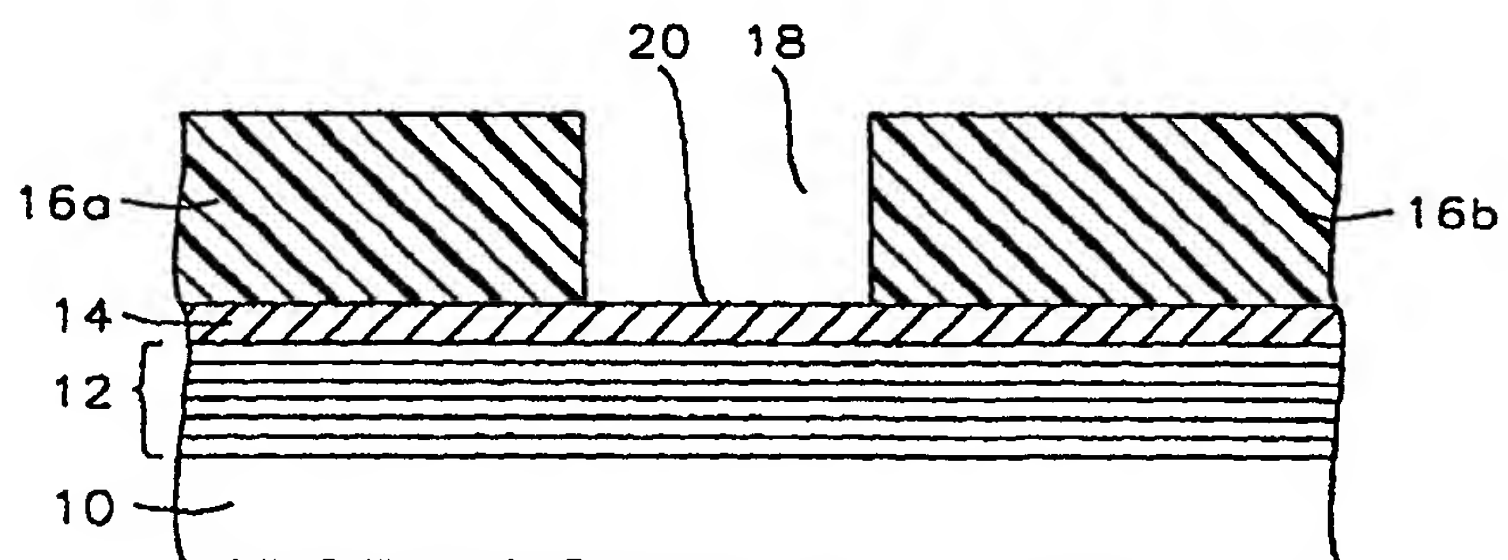


FIG. 6

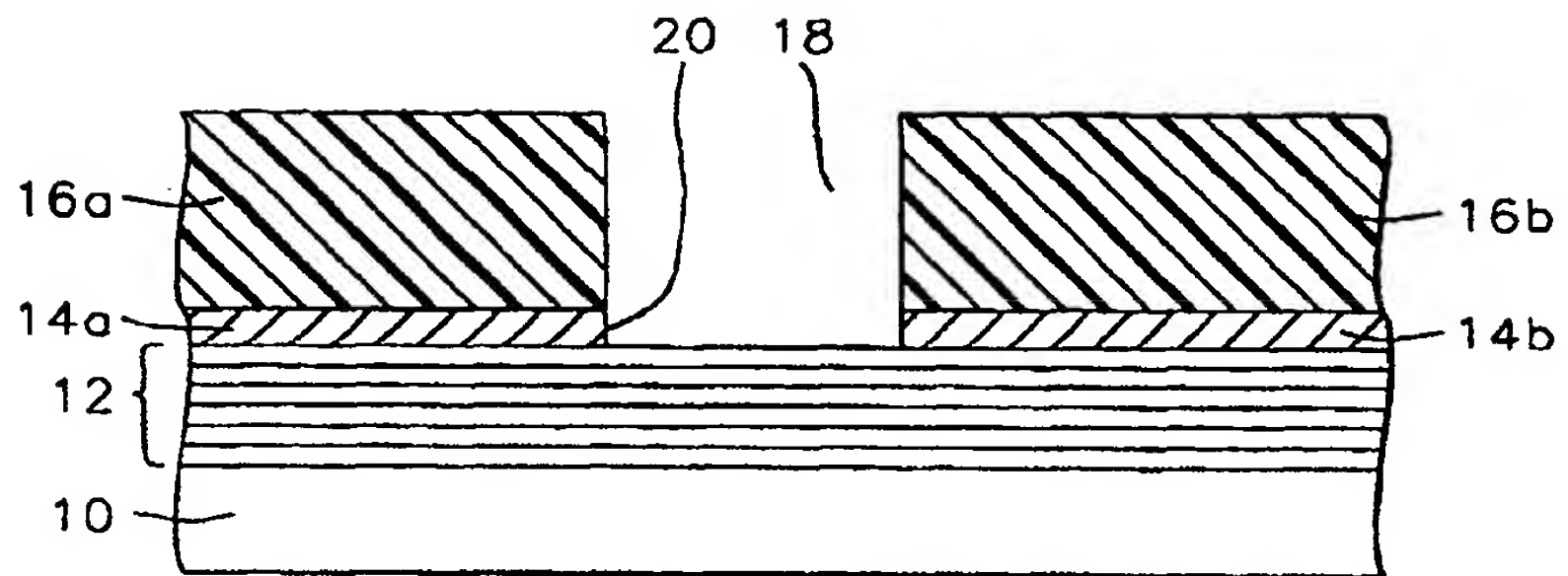


FIG. 7

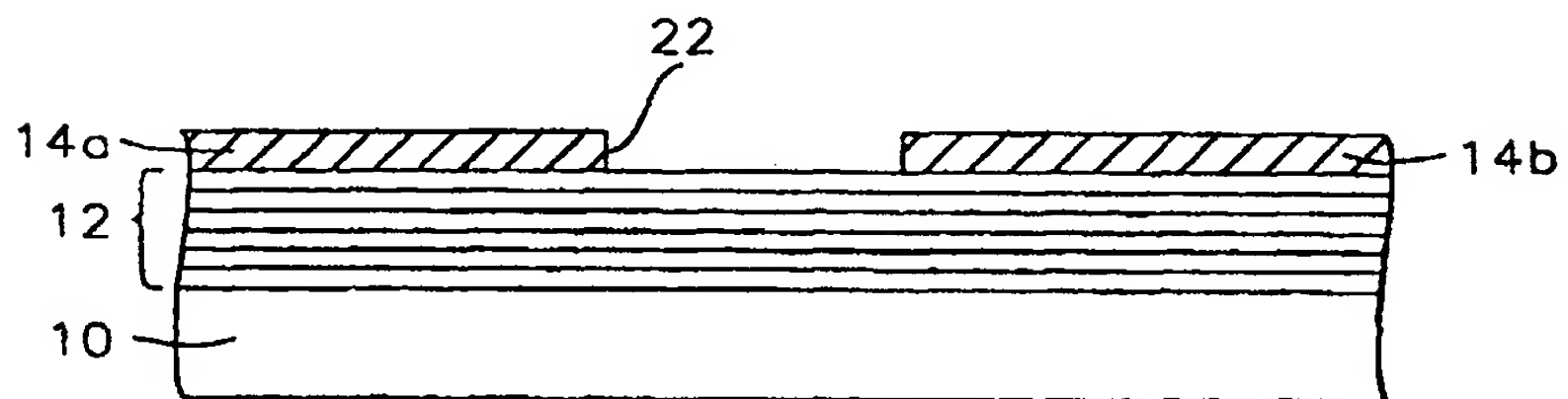


FIG. 8





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(72) Inventors:  
• **Subhash, Gupta**  
**Singapore 259805 (SG)**  
• **Mei-Sheng, Shou**  
**129791 Singapore (SG)**

(30) Priority: **20.02.2001 US 785116**

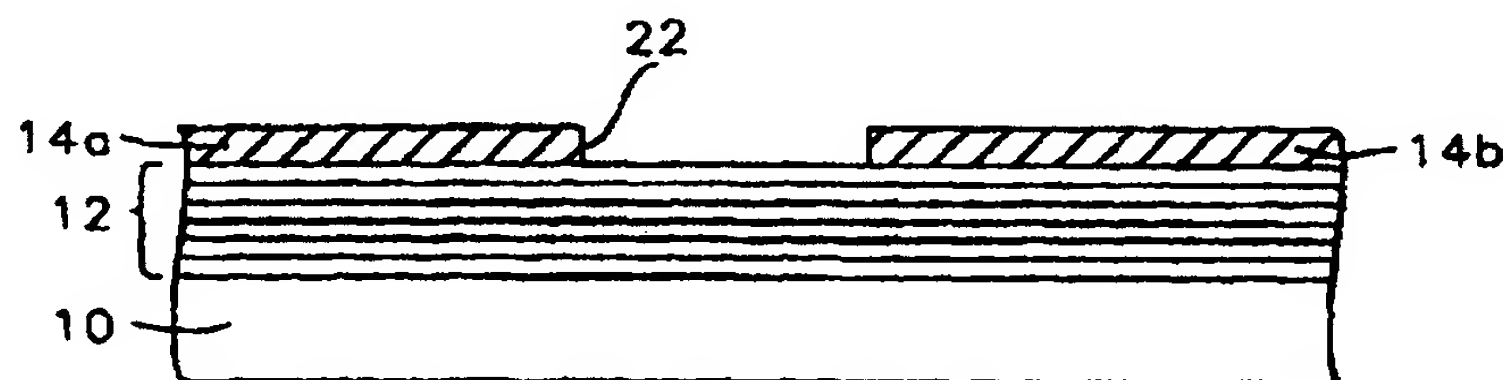
(74) Representative: **Schuffenecker, Thierry**  
**97, chemin de Cassiopée,**  
**Domaine de l'étoile**  
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**FIG. 8**



European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number  
EP 02 36 8019

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
X	US 5 641 593 A (WATANABE YUTAKA ET AL) 24 June 1997 (1997-06-24) * column 6, line 62 - line 66; claims 3,5; figure 1 * * claim 1; figure 5 * * column 12, line 47 - line 49 *	1-4,6-15	G03F1/14 G03F1/08
X	EP 0 671 658 A (CANON KK) 13 September 1995 (1995-09-13) * page 6, line 39 - line 40; figure 3 * * page 7, line 1 - line 10 * * page 5, line 27 - line 32 *	1-4,6-15	
			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
			G03F
The present search report has been drawn up for all claims			
Place of search MUNICH		Date of completion of the search 27 August 2002	Examiner Angioni, C
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons</p> <p>&amp; : member of the same patent family, corresponding document</p>			

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**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 02 36 8019

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on  
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